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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2816

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TANIGUCHI

Art Unit: 2816

Application No. 09/777,897

Examiner: L. Nguyen

Filed: February 7, 2001

Atty. Docket No. 100353-00039

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF AN OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN AN INPUT SIGNAL AND THE OUTPUT SIGNAL BECOMES AN INTEGRAL NUMBER OF PERIODS OTHER THAN ZERO

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

May 9, 2002

Sir:

In reply to the Office Action mailed February 11, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1 and 3-7 as follows:

Sub
B1 D1

1. (Twice Amended) A delay time adjusting method of adjusting a delay time of an input signal so that a phase of said input signal and a phase of an output signal match each other, based on a comparison between phases of said input signal and said output signal, the method comprising the steps of:

increasing the delay time to adjust said phase of said output signal irrespective of said comparison when starting the delay time adjustment.

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